Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6602	709/232 370/229 710/61	US-PGPUB; USPAT; EPO	OR	ON	2006/04/01 20:56
L2	18	1 and (assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	ON	2006/04/01 20:28
L3	514	1 and (schedule\$3 assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit)	USPAT	OR	ON	2006/04/01 20:57
L4	62	1 and (schedule\$3 assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) same table	USPAT	OR	ON	2006/04/01 20:36
L5	2	1 and (schedule\$3 assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) same table with identifier	USPAT	OR	ON	2006/04/01 20:57
L6	1703	(schedule\$3 assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) same table	US-PGPUB; USPAT; EPO	OR	ON	2006/04/01 20:36
L7	72	(schedule\$3 assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) same table with identifier	US-PGPUB; USPAT; EPO	OR	ON	2006/04/01 20:58
L8	0	"manian radesh"".in" "sathe shirish k"".in"	US-PGPUB; USPAT	OR	ON	2006/04/01 20:41
L10	6	"manian radesh".in. "sathe shirish $k$ ". in.	US-PGPUB; USPAT	OR	ON	2006/04/01 20:42
L11	0	("manian radesh".in. "sathe shirish k". in.) and (schedule adj connection). clm.	US-PGPUB; USPAT	OR	ON	2006/04/01 20:43
L12	2	("manian radesh".in. "sathe shirish k". in.) and (connection).clm.	US-PGPUB; USPAT	OR	ON	2006/04/01 20:44
L13	15172	709/232 709/230 370/229 370/230 370/395 370/398 710/61	US-PGPUB; USPAT; EPO	OR	ON	2006/04/01 20:56
L14	10	13 and (schedule\$3 assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) same table with identifier	USPAT	OR	ON	2006/04/01 20:57
L15	14	13 and (schedule\$3 assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) same table with identifier	US-PGPUB; USPAT; EPO	OR	ON	2006/04/01 20:57

L16  14 13 and (schedule\$3 assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) same table with identifier  S1 135 ("6058114" "6370144" "6389031" "5631908" "5999533" "6144637" "6167049" "5844901" "5974466" EPO; JPO; "6262969" "6272109" "6208661" "5993566" "5999958" "611798" "6115382" "6262989" "6272109" "6208661" "593374" "5809024" "5852606" "5889779" "6005866" "5989959" "571288" "618305" "6130878" "6085221" "6088734" "6108305" "6130878" "6260999" "6373811" "6307251" "6408005" "5818839" "5867657" "5515363" "5724513" "6167059" "5712851" "5712851" "5712851" "5712871" "6232423" "6229812" "5555378" "6198724" "6137779" "5982771" "6233243" "6295488" "6229812" "557604729" "5740158" "5463620" "3963533" "6122279" "6266706" (5332994" "6134246" (63333846" "398024" "65373294" "6136468" "5793052" "614635" "576032" "6269082" "5712851" "57804729" "5740158" "5463620" "5930944" "6137779" "582771 "5804729" "5740158" "5463620" "593094" "6134246" (63333846" "4969092" "613102" "6081524" "6186671" "4581736" "6097958" "6118355" "6128303" "5748630" "57518628" "6188671" "4581736" "6097958" "6118355" "6128303" "5748630" "5751867" "5739025" "5860148" "5884297" "5838681" "5987031" "6026095"				•			
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"6359891").pn.	S1	135	"5631908" "5999533" "6144637" "6167049" "5844901" "5974466" "6262976" "6011798" "6115382" "6262989" "6272109" "6208661" "5923656" "5995995" "5734656" "5737334" "5809024" "5852606" "5889779" "6005866" "5418779" "5629937" "5734650" "5771228" "5781531" "6046998" "6085221" "6088734" "6108305" "6130878" "6269079" "6337851" "6397251" "6408005" "5818839" "5867657" "5515363" "5724513" "6167059" "5612959" "6201809" "5991867" "5748631" "5793747" "5796735" "5712851" "5751709").pn. ("5914934" "6205118" "6407992" "5563885" "6229812" "5555378" "6198724" "6137779" "5982771" "6233243" "5694548" "6092113" "5604729" "5740158" "5463620" "5963553" "6122279" "6266706" "6392994" "6047326" "5579302" "6018527" "6144635" "5765032" "6269082" "5726985" "5841772" "5848068" "5920561" "5982749" "5917828" "6134246" "6373846" "4969092" "6151302" "6081524" "6188671" "4581736" "6097958" "6141355" "6128303" "5748630" "5751967" "5794025" "5860148" "5884297" "5794025" "5860148" "5884297" "5794025" "5860148" "5884297" "57838681" "5987031" "6026095"	USPAT; EPO; JPO;	OR	ON	2002/07/28 15:14

S2 0	((("6058114" "6370144" "6389031" "5631908" "5999533" "6144637"	US-PGPUB;	OR	ON	2002/07/28 15:46
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- 1		"5748631" "5793747" "5796735"				
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İ		"6205118" "6407992" <sup>*</sup> "5563885"				
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S4	0	(("6058114" "6370144" "6389031"	US-PGPUB;	OR	ON	2002/07/28 15:46
<b>J</b> 7	1	"5631908" "5999533" "6144637"	USPAT;		•••	2002/07/20 13:40
		"6167049" "5844901" "5974466"	EPO; JPO;			
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		schedule adj table				

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S5 0	0 (("6058114" "6370144" "6389031" "5631908" "5999533" "6144637" "6167049" "5844901" "5974466" "6262976" "6011798" "6115382" "6262989" "6272109" "6208661" "5923656" "5995995" "5734656" "5737334" "5809024" "5852606" "5889779" "6005866" "5418779" "5629937" "5734650" "5771228" "5781531" "6046998" "6085221" "6088734" "6108305" "6130878" "6269079" "6337851" "6397251" "6408005" "5818839" "5867657" "5515363" "5724513" "6167059" "5748631" "5793747" "5796735" "5712851" "5751709").pn. ("5914934" "6205118" "6407992" "5563885" "6229812" "55555378" "6198724" "6137779" "5982771" "6233243" "5694548" "6092113" "5604729" "5740158" "5463620" "5963553" "6122279" "6266706" "6392994" "6047326" "5579302" "6018527" "6144635" "5765032" "6269082" "5726985" "5841772" "5848068" "5920561" "5982749" "5917828" "6134246" "6373846" "4969092" "6151302" "6081524" "6188671" "4581736" "6097958" "6141355" "6128303" "5748630" "5751967" "5794025" "5860148" "5884297" "5838681" "5987031" "6026095" "5383681" "5987031" "6026095" "5383681" "5987031" "6026095" "5835881" "5987031" "6026095" "5835881" "5987031" "6026095" "5835881" "5987031" "6026095" "5835881" "5987031" "6026095" "5838681" "5987031" "6026095" "58359891").pn. ) and divid\$3 adj	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2002/07/28 15:47

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S6	19	(("6058114" "6370144" "6389031" "5631908" "5999533" "6144637" "6167049" "5844901" "5974466" "6262976" "6011798" "6115382" "6262989" "6272109" "6208661" "5923656" "5995995" "5734656" "5737334" "5809024" "5852606" "5889779" "6005866" "5418779" "5629937" "5734650" "5771228" "5781531" "6046998" "6085221" "6088734" "6108305" "6130878" "6269079" "6337851" "6397251" "6408005" "5818839" "5867657" "5515363" "5724513" "6167059" "5612959" "6201809" "5991867" "5748631" "5793747" "5796735" "5712851" "5751709").pn. ("5914934" "6205118" "6407992" "5563885" "6229812" "5555378" "6198724" "6137779" "5982771" "6233243" "5694548" "6092113" "5604729" "5740158" "5463620" "5963553" "6122279" "6266706" "6392994" "6047326" "5579302" "6018527" "6144635" "5765032" "6269082" "5726985" "5841772" "5848068" "5920561" "5982749" "5917828" "6134246" "6373846" "4969092" "6151302" "6081524" "6188671" "4581736" "6097958" "6141355" "6128303" "5748630" "5751967"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2002/07/28 16:27
		"5794025" "5860148" "5884297" "5838681" "5987031" "6026095" "6359891").pn. ) and schedule adj table				
S7	1	"6272109".URPN.	USPAT	OR	ON	2002/07/28 16:04
S8	4	"6005866".URPN.	USPAT	OR	ON	2002/07/28 16:18
S9	14	("5706288"   "5719853"   "5751709"   "5774653"   "5838677"   "5850395"   "5864540"   "5889779"   "5892762"   "5909443"   "6005866"   "6011775"   "6028843"   "6226265").PN.	USPAT	OR	ON	2002/07/28 16:24
S10	0	"6247061".URPN.	USPAT	OR	ON	2002/07/28 16:25
S11	24	("5463624"   "5517622"   "5521923"   "5553061"   "5555264"   "5724513"   "5790522"   "5796956"   "5819043"   "5822317"   "5831971"   "5884037"   "5889956"   "5903735"   "5917822"   "5935218"   "5974465"   "5983278"   "5987522"   "5996013"   "5996020"   "6003086"   "6005866"   "6011775"). PN.	USPAT	OR	ON	2002/07/28 16:25

		EAST Scarci				
S12	0	ATM adj traffic adj schedule3\$	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2002/07/28 16:41
S13	0	ATM adj network and schedule3\$	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2002/07/28 16:42
S14	5185	ATM adj network	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2002/07/28 16:42
S15	1116	(ATM adj network ) and schedul\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:17
S16	1	"6272109".URPN.	USPAT	OR	ON	2002/07/28 21:41
S17	2	"6272109".pn.	USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2003/01/13 13:44
S18	87	(ATM adj network ) and schedul\$4 adj table\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/08/07 19:52
S19	0	ATM adj traffic adj scheduler	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/08/07 19:56
S20	19	ATM adj scheduler	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/08/07 19:56
S21	2	(ATM adj network ) and schedul\$4 adj table\$1 and delimiter\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/08/07 19:56
S22	0	(ATM adj network ) and schedul\$4 adj table\$1 and table adj delimiter\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/08/07 19:57
S23	0	(ATM adj network ) and schedul\$4 adj table\$1 and delimeter\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/08/07 19:57
S24	0	(ATM adj network ) and schedul\$4 adj table\$1 and table adj delimeter\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/08/07 19:57

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S25	0	((ATM adj network ) and schedul\$4 adj table\$1) and delimeter\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/08/07 19:57
S26	1	. ATM adj traffic adj schedule\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:35
S27	11371	network adj traffic	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:17
S28	0	network adj traffic with (schedule adj table)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:18
S29	0	network adj traffic with (multiple adj schedule )	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:19
S30	9	network adj traffic and (multiple adj schedule )	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:26
S31	194	sathe.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:26
S32	7	sathe.in. and shirish	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:26
S33	5	hardware adj schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 15:38
S34	0	logical adj schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:36
S35	78	(connection traffic (class adj service))with schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:55
S36	0	(connection traffic (class adj service))with schedule adj table.ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:48

S37	19	(connection traffic (class adj service))with schedule adj table.ab.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 19:48
S38	1	("2001/0010717").URPN.	USPAT	OR	OFF	2005/01/02 19:51
S39	62	(connection traffic (class adj service))with schedule adj table and @ad<"19991129"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 20:43
S40	1	"6667977".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 20:17
S41	19	("6272109").URPN.	USPAT	OR	OFF	2005/01/02 20:21
S42	0	(class adj service) with schedule adj table and @ad<"19991129"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 20:26
S43	0	(class adj service) with schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/02 20:26
S44	13	("6122657").URPN.	USPAT	OR	OFF	2005/01/07 12:26
S45	15	("20020026321"   "4890098"   "5659729"   "5708709"   "5742768"   "5978847"   "6009441"   "6021416"   "6031530"   "6112242"   "6122657"   "6138237"   "6311214"   "6442549"   "6446192").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/01/07 12:27
S46	13	("600 <del>944</del> 1").URPN.	USPAT	OR	OFF	2005/01/07 12:39
S47	5	hardware adj schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 16:02
S48	7	schedul\$4 near3 traffic near3 schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 16:03
S49	6	("6262989").URPN.	USPAT	OR	OFF	2005/01/08 16:39
S50	0	(connection traffic (class adj service))with schedule adj table.ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 16:41
S51	19	(connection traffic (class adj service))with schedule adj table.ab.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 16:42

S52	78	(connection traffic (class adj service))with schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 16:50
S53	75	sar adj processor	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 17:16
S55	0	sar adj processor with schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 16:52
S56	7	sar adj processor and schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 18:35
S57	7	sar adj processor and schedule adj table and ((class\$3 adj service) COS)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 17:03
S58	24	sar adj processor and ((class\$3 adj service) COS)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 17:03
S59	17	S58 not S57	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 17:04
S62	31	sar adj processor and "370"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 17:16
S63	12	("5982749").URPN.	USPAT	OR	OFF	2005/01/08 18:08
S64	1	("6512741").URPN.	USPAT	OR	OFF	2005/01/08 18:17
S65	2	("6430187").URPN.	USPAT	OR	OFF	2005/01/08 18:32
S66	0	sar adj processor and (variable "not fixed") adj schedul\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 18:36
S67	14	sar adj processor and schedul\$4 and "370"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 18:53
S68	0	sar and variable adj traffic adj (schedul\$4 shap\$3) and "370"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 18:54

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S69	112	sar and variable and traffic adj (schedul\$4 shap\$3) and "370"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 18:54
S70	7	sar adj processor and variable and traffic adj (schedul\$4 shap\$3) and "370"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/08 18:54
S71	1	("6285657").URPN.	USPAT	OR	OFF	2005/01/08 19:28
S72	0	ATM and divid\$3 adj shcedule adj table	USPAT	OR	OFF	2005/01/08 19:48
S73	0	ATM and divid\$3 adj schedule adj table	USPAT	OR	OFF	2005/01/08 19:48
S74	0	ATM and (divid\$3 partion\$3) adj schedule adj table	USPAT	OR	OFF	2005/01/08 19:50
S75	89	ATM and schedule adj table	USPAT	OR	OFF	2005/01/08 19:49
S76	1	ATM and (creating divid\$3 partion\$3 aasign\$3) adj schedule adj table	USPAT	OR	OFF	2005/01/08 19:51
S77	3	ATM and (creating divid\$3 partion\$3 aasign\$3) adj schedule	USPAT	OR	OFF	2005/01/08 19:53
S78	0	sar and (creating divid\$3 partion\$3 aasign\$3) adj schedule and "370"	USPAT	OR	OFF	2005/01/08 19:53
S79	0	sar and (creating divid\$3 partion\$3 aasign\$3) adj schedule	USPAT	OR	OFF	2005/01/08 20:05
S80	3	dynamic adj schedul\$3 with (virtual adj (channel circuit)) and ATM	USPAT	OR	OFF	2005/01/08 21:21
S81	107	(dynamic schedul\$3) with traffic adj shap\$3 and ATM	USPAT	OR	OFF	2005/01/08 21:22
S82	27	sar and (dynamic schedul\$3) with traffic adj shap\$3 and ATM	USPAT	OR	OFF	2005/01/08 21:22
S83	1	sar and (dynamic schedul\$3) with traffic adj shap\$3 and ATM and "307"	USPAT	OR	OFF	2005/01/08 21:23
S84	27	sar and (dynamic schedul\$3) with traffic adj shap\$3 and ATM	USPAT	OR	OFF	2005/01/08 21:49
S85	6	sar and (dynamic schedul\$3) with traffic and ATM and scoreboard	USPAT	OR	OFF	2005/01/08 22:10
S86	4	scoreboard.ti. and "370"	USPAT	OR	OFF	2005/01/08 22:11
S87	7	((schedule adj table )scoreboard).ti. and "370"	USPAT	OR	OFF	2005/01/09 12:24
S88	4	("6359891").URPN.	USPAT	OR	OFF	2005/01/08 22:24
589	82	((schedule adj table )scoreboard)and ATM and "370"	USPAT	OR	OFF	2005/01/09 13:09
S90	3	("6477144").URPN.	USPAT	OR	OFF	2005/01/09 12:53

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591	11	("4926418"   "5185861"   "5909594"   "5924098"   "5936956"   "5960434"   "6317872"   "6418517"   "6446225"   "6477144"   "6487212").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/01/09 12:57
S92	42	((schedule adj table )scoreboard)and ATM and "370" and (segmentation near2 reassembly)	USPAT	OR	OFF	2005/01/09 14:31
S93	1	((schedule adj table )scoreboard)and (channel traffic path circuit) adj (allocat\$3 reservat\$3)and ATM and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR .	OFF	2005/01/09 15:30
594	1	((schedule adj table )scoreboard)and (channel traffic path circuit) adj (allocat\$3 reservat\$3)and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	OFF	2005/01/09 16:30
S95	2	((schedul\$3 adj table )scoreboard)and (channel traffic path circuit) adj (assign\$3 allocat\$3 reservat\$3)and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	OFF	2005/01/09 15:35
S96	41	((schedul\$3 )scoreboard)with (channel traffic path circuit) with (assign\$3 allocat\$3 reservat\$3)and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	OFF	2005/01/09 15:56
S97	1	("5889763").URPN.	USPAT	OR .	OFF	2005/01/09 15:53
S98	72	(assign\$3 allocat\$3 reservat\$3) with (channel traffic path circuit) near5 connection and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	ON	2005/01/09 16:35
S99	9	(assign\$3 allocat\$3 reservat\$3) with (channel traffic path circuit) near5 connection with logical and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	ON	2005/01/09 16:00
S10 0	112	(assign\$3 allocat\$3 reservat\$3 management ) with (channel traffic path circuit) near5 connection and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR .	ON	2005/01/09 16:40
S10 1	73	(assign\$3 allocat\$3 reservat\$3 management ) with (channel traffic path circuit) near5 connection and ((sar adj processor)(segmentation near2 reassembly)) and logical	USPAT	OR	ON	2005/01/09 16:38

S10 2	0	logical adj schedule adj table	USPAT	OR	ON	2005/01/09 16:39
S10 3	683	schedule adj table	USPAT	OR	ON	2005/01/09 16:39
S10 4	3	logical with schedule adj table	USPAT	OR	ON	2005/01/09 16:39
S10 5	7	(assign\$3 allocat\$3 reservat\$3 management ) adj (channel traffic path circuit) near5 connection and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	ON	2005/01/09 16:44
S10 6	186	(assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	ON	2006/04/01 20:27
S10 7	3	(assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly)).ti.	USPAT	OR	ON	2005/01/09 16:48
S10 8	168	(assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))and ATM	USPAT	OR	ON	2005/01/09 16:57
S10 9	27	(allocat\$3 manag\$6 ) adj virtual adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))and ATM	USPAT	OR	ON	2005/01/09 17:05
S11 0	0	(allocat\$3 manag\$6 ) adj virtual adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))and ATM and scorboard	USPAT	OR	ON	2005/01/09 19:10
S11 1	0	(allocat\$3 manag\$6 ) adj virtual adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))and ATM and scoreboard	USPAT	OR	ON	2005/01/09 17:09
S11 2	6	(allocat\$3 manag\$6 ) with virtual adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))and ATM and scoreboard	USPAT	OR	ON	2005/01/09 18:49
S11 3	1	("5889763").URPN.	USPAT	OR	OFF	2005/01/09 17:33
S11 4	7	((sar adj processor)(segmentation near2 reassembly)) and scoreboard	USPAT	OR	ON	2005/01/09 19:11

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S11 5	0	((sar adj processor)(segmentation near2 reassembly)) with virtual adj (channel traffic path circuit) adj (alloca\$3 implementat\$3)	USPAT	OR	ON	2005/01/09 19:12
S11 6	11	((sar adj processor)(segmentation near2 reassembly)) and virtual adj (channel traffic path circuit) adj (alloca\$3 implementat\$3)	USPAT	OR	ON	2005/01/09 19:14
S11 7	12	((sar )(segmentation near2 reassembly)) and virtual adj (channel traffic path circuit) adj (alloca\$3 implementat\$3)	USPAT	OR	ON	2005/01/09 19:14
S11 8	5	"6359891"	USPAT	OR	ON	2005/07/23 20:38
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		"6144635" "5765032" "6269082"				
		"5726985" "5841772" "5848068"				
		"5920561" "5982749" "5917828"				
		"6134246" "6373846" "4969092"				
		"6151302" "6081524" "6188671"				
		"4581736" "6097958" "6141355"				
		"6128303" "5748630" "5751967"				
		"5794025" "5860148" "5884297"				
		"5838681" "5987031" "6026095"				
		"6359891").pn. ) and divid\$3 adj				
		schedule adj table				

		<del></del>	T	т	
S12 18	(("6058114" "6370144" "6389031" "5631908" "5999533" "6144637" "6167049" "5844901" "5974466" "6262976" "6011798" "6115382" "6262989" "6272109" "6208661" "5923656" "5995995" "5734656" "5737334" "5809024" "5852606" "5889779" "6005866" "5418779" "5629937" "5734650" "5771228" "5781531" "6046998" "6085221" "6088734" "6108305" "6130878" "6269079" "6337851" "6397251" "6408005" "5818839" "5867657" "5515363" "5724513" "6167059" "5612959" "6201809" "5991867" "5748631" "5793747" "5796735" "5712851" "5751709").pn. ("5914934" "6205118" "6407992" "5563885" "6229812" "55555378" "6198724" "6137779" "5982771" "6233243" "5694548" "6092113" "5604729" "5740158" "5463620" "5963553" "6122279" "6266706" "6392994" "6047326" "5579302" "6018527" "6144635" "5765032" "6269082" "5726985" "5841772" "5848068" "5920561" "5982749" "5917828" "6134246" "6373846" "4969092" "6151302" "6081524" "6188671" "4581736" "6097958" "6141355" "6128303" "5748630" "5751967" "5794025" "5860148" "5884297" "5838681" "5987031" "6026095" "6359891").pn. ) and schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S12 21 6	"6272109".URPN.	USPAT	OR	ON	2006/03/31 22:08
S12 10 7	"6005866".URPN.	USPAT	OR	ON	2006/03/31 22:08
S12 14 8	("5706288"   "5719853"   "5751709"   "5774653"   "5838677"   "5850395"   "5864540"   "5889779"   "5892762"   "5909443"   "6005866"   "6011775"   "6028843"   "6226265").PN.	USPAT	OR	ON	2006/03/31 22:08
S12 14 9	"6247061".URPN.	USPAT	OR	ON	2006/03/31 22:08

S13 0	24	("5463624"   "5517622"   "5521923"   "5553061"   "5555264"   "5724513"   "5790522"   "5796956"   "5819043"   "5822317"   "5831971"   "5884037"   "5889956"   "5903735"   "5917822"   "5935218"   "5974465"   "5983278"   "5987522"   "5996013"   "5996020"   "6003086"   "6005866"   "6011775"). PN.	USPAT	OR	ON	2006/03/31 22:08
S13 1	0	ATM adj traffic adj schedule3\$	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S13 2	0	ATM adj network and schedule3\$	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S13 3	10530	ATM adj network	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S13 4	2566	(ATM adj network ) and schedul\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S13 5	21	"6272109".URPN.	USPAT	OR	ON	2006/03/31 22:08
S13 6	2	"6272109".pn.	USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 22:08
S13 7	114	(ATM adj network ) and schedul\$4 adj table\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S13 8	0	ATM adj traffic adj scheduler	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S13 9	31	ATM adj scheduler	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S14 0	9	(ATM adj network ) and schedul\$4 adj table\$1 and delimiter\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08

		LASI Scarci	,			
S14 1	. 0	(ATM adj network ) and schedul\$4 adj table\$1 and table adj delimiter\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S14 2	0	(ATM adj network ) and schedul\$4 adj table\$1 and delimeter\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S14 3	0	(ATM adj network ) and schedul\$4 adj table\$1 and table adj delimeter\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S14 4	0	((ATM adj network ) and schedul\$4 adj table\$1) and delimeter\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S14 5	2	ATM adj traffic adj schedule\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S14 6	15648	network adj traffic	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S14 7	0	network adj traffic with (schedule adj table)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S14 8	0	network adj traffic with (multiple adj schedule )	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S14 9	12	network adj traffic and (multiple adj schedule )	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S15 0	220	sathe.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S15 1	8	sathe.in. and shirish	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S15 2	6	hardware adj schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08

S15 3	0	logical adj schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S15 4	0	(connection traffic (class adj service))with schedule adj table.ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S15 5	19	(connection traffic (class adj service))with schedule adj table.ab.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S15 6	1	("2001/0010717").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S15 7	85	(connection traffic (class adj service))with schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR .	ON	2006/03/31 22:08
S15 8	63	(connection traffic (class adj service))with schedule adj table and @ad<"19991129"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S15 9	1	"6667977".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S16 0	21	("6272109").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S16 1	0	(class adj service) with schedule adj table and @ad<"19991129"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S16 2	0	(class adj service) with schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S16 3	16	("6122657").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S16 4	. 15	("20020026321"   "4890098"   "5659729"   "5708709"   "5742768"   "5978847"   "6009441"   "6021416"   "6031530"   "6112242"   "6122657"   "6138237"   "6311214"   "6442549"   "6446192").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/31 22:08
S16 5	16	("6009441").URPN.	USPAT	OR	OFF	2006/03/31 22:08

			_			
S16 6	6	hardware adj schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S16 7	7	schedul\$4 near3 traffic near3 schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S16 8	9	("6262989").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S16 9	0	(connection traffic (class adj service))with schedule adj table.ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S17 0	19	(connection traffic (class adj service))with schedule adj table.ab.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S17 1	85	(connection traffic (class adj service))with schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S17 2	0	sar adj processor with sdhedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S17 3	0	sar adj processor with schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S17 4	7	sar adj processor and schedule adj table	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S17 5	7	sar adj processor and schedule adj table and ((class\$3 adj service) COS)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S17 6	27	sar adj processor and ((class\$3 adj service) COS)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S17 7	20	S176 not S175	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08

S17 8	81	sar adj processor	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S17 9	27	sar adj processor and "370"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S18 0	14	("5982749").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S18 1	3	("6512741").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S18 2	4	("6430187").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S18 3	0	sar adj processor and (variable "not fixed") adj schedul\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S18 4	12	sar adj processor and schedul\$4 and "370"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S18 5	0	sar and variable adj traffic adj (schedul\$4 shap\$3) and "370"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S18 6	7	sar adj processor and variable and traffic adj (schedul\$4 shap\$3) and "370"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S18 7	105	sar and variable and traffic adj (schedul\$4 shap\$3) and "370"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 22:08
S18 8	2	("6285657").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S18 9	0	ATM and divid\$3 adj shcedule adj table	USPAT	OR	OFF	2006/03/31 22:08
S19 0	0	ATM and divid\$3 adj schedule adj table	USPAT	OR	OFF	2006/03/31 22:08
S19 1	0	ATM and (divid\$3 partion\$3) adj schedule adj table	USPAT	OR	OFF	2006/03/31 22:08
S19 2	98	ATM and schedule adj table	USPAT	OR	OFF	2006/03/31 22:08
S19 3	1	ATM and (creating divid\$3 partion\$3 aasign\$3) adj schedule adj table	USPAT	OR	OFF	2006/03/31 22:08

S19 4	4	ATM and (creating divid\$3 partion\$3 aasign\$3) adj schedule	USPAT	OR	OFF	2006/03/31 22:08
S19 5	0	sar and (creating divid\$3 partion\$3 aasign\$3) adj schedule and "370"	USPAT	OR	OFF	2006/03/31 22:08
S19 6	0	sar and (creating divid\$3 partion\$3 aasign\$3) adj schedule	USPAT	OR	OFF	2006/03/31 22:08
S19 7	3	dynamic adj schedul\$3 with (virtual adj (channel circuit)) and ATM	USPAT	OR	OFF	2006/03/31 22:08
S19 8	123	(dynamic schedul\$3) with traffic adj shap\$3 and ATM	USPAT	OR	OFF	2006/03/31 22:08
S19 9	28	sar and (dynamic schedul\$3) with traffic adj shap\$3 and ATM	USPAT	OR	OFF	2006/03/31 22:08
S20 0	1	sar and (dynamic schedul\$3) with traffic adj shap\$3 and ATM and "307"	USPAT	OR	OFF	2006/03/31 22:08
S20 1	28	sar and (dynamic schedul\$3) with traffic adj shap\$3 and ATM	USPAT	OR	OFF	2006/03/31 22:08
S20 2	6	sar and (dynamic schedul\$3) with traffic and ATM and scoreboard	USPAT	OR	OFF	2006/03/31 22:08
S20 3	4	scoreboard.ti. and "370"	USPAT	OR	OFF	2006/03/31 22:08
S20 4	7	((schedule adj table )scoreboard).ti. and "370"	USPAT	OR	OFF	2006/03/31 22:08
S20 5	6	("6359891").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S20 6	90	((schedule adj table )scoreboard)and ATM and "370"	USPAT	OR	OFF	2006/03/31 22:08
S20 7	12	("6477144").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S20 8	11	("4926418"   "5185861"   "5909594"   "5924098"   "5936956"   "5960434"   "6317872"   "6418517"   "6446225"   "6477144"   "6487212").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/31 22:08
S20 9	43	((schedule adj table )scoreboard)and ATM and "370" and (segmentation near2 reassembly)	USPAT	OR	OFF	2006/03/31 22:08
S21 0	2	((schedule adj table )scoreboard)and (channel traffic path circuit) adj (allocat\$3 reservat\$3)and ATM and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	OFF	2006/03/31 22:08
S21 1	3	((schedul\$3 adj table )scoreboard)and (channel traffic path circuit) adj (assign\$3 allocat\$3 reservat\$3)and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	OFF	2006/03/31 22:08

S21 2	44	((schedul\$3 )scoreboard)with (channel traffic path circuit) with (assign\$3 allocat\$3 reservat\$3)and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	OFF	2006/03/31 22:08
S21 3	2	("5889763").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S21 4	88	(assign\$3 allocat\$3 reservat\$3) with (channel traffic path circuit) near5 connection and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	ON	2006/03/31 22:08
S21 5	9	(assign\$3 allocat\$3 reservat\$3) with (channel traffic path circuit) near5 connection with logical and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	ON	2006/03/31 22:08
S21 6	2	((schedule adj table )scoreboard)and (channel traffic path circuit) adj (allocat\$3 reservat\$3)and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	OFF	2006/03/31 22:08
S21 7	87	(assign\$3 allocat\$3 reservat\$3 management ) with (channel traffic path circuit) near5 connection and ((sar adj processor)(segmentation near2 reassembly)) and logical	USPAT	OR	ON	2006/03/31 22:08
S21 8	0	logical adj schedule adj table	USPAT	OR	ON	2006/03/31 22:08
S21 9	747	schedule adj table	USPAT	OR	ON	2006/03/31 22:08
S22 0	4	logical with schedule adj table	USPAT	OR	ON	2006/03/31 22:08
S22 1	132	(assign\$3 allocat\$3 reservat\$3 management ) with (channel traffic path circuit) near5 connection and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	ON	2006/03/31 22:08
S22 2	8	(assign\$3 allocat\$3 reservat\$3 management ) adj (channel traffic path circuit) near5 connection and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	ON	2006/03/31 22:08
S22 3	216	(assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))	USPAT	OR	ON	2006/03/31 22:08

S22 4	3	(assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly)).ti.	USPAT	OR	ON	2006/03/31 22:08
S22 5	197	(assign\$3 allocat\$3 reservat\$3 manag\$6 ) adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))and ATM	USPAT	OR	ON	2006/03/31 22:08
S22 6	31	(allocat\$3 manag\$6 ) adj virtual adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))and ATM	USPAT	OR	ON	2006/03/31 22:08
S22 7	0	(allocat\$3 manag\$6 ) adj virtual adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))and ATM and scorboard	USPAT	OR	ON	2006/03/31 22:08
S22 8	0	(allocat\$3 manag\$6 ) adj virtual adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))and ATM and scoreboard	USPAT	OR	ON	2006/03/31 22:08
S22 9	6	(allocat\$3 manag\$6 ) with virtual adj (channel traffic path circuit) and ((sar adj processor)(segmentation near2 reassembly))and ATM and scoreboard	USPAT	OR	ON	2006/03/31 22:08
S23 0	2	("5889763").URPN.	USPAT	OR	OFF	2006/03/31 22:08
S23 1	8	((sar adj processor)(segmentation near2 reassembly)) and scoreboard	USPAT	OR	ON	2006/03/31 22:08
S23 2	0	((sar adj processor)(segmentation near2 reassembly)) with virtual adj (channel traffic path circuit) adj (alloca\$3 implementat\$3)	USPAT	OR	ON	2006/03/31 22:08
S23 3	13	((sar adj processor)(segmentation near2 reassembly)) and virtual adj (channel traffic path circuit) adj (alloca\$3 implementat\$3)	USPAT	OR	ON	2006/03/31 22:08
S23 4	14	((sar )(segmentation near2 reassembly)) and virtual adj (channel traffic path circuit) adj (alloca\$3 implementat\$3)	USPAT	OR	ON	2006/03/31 22:08
S23 5	7	"6359891"	USPAT	OR	ON	2006/03/31 22:08



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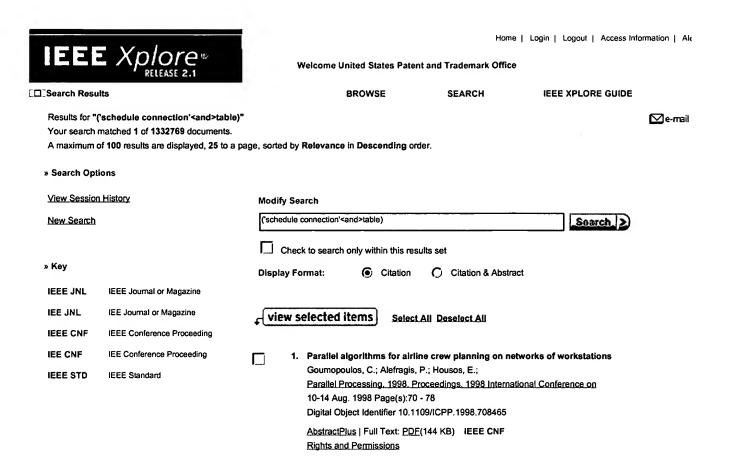
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Best 200 shown

Relevance scale

1 4.2BSD and 4.3BSD as examples of the UNIX system



John S. Quarterman, Abraham Silberschatz, James L. Peterson December 1985 ACM Computing Surveys (CSUR), Volume 17 Issue 4

window

Publisher: ACM Press

Full text available: pdf(4.07 MB)

Additional Information: full citation, abstract, references, citings, index terms, review

This paper presents an in-depth examination of the 4.2 Berkeley Software Distribution, Virtual VAX-11 Version (4.2BSD), which is a version of the UNIX Time-Sharing System. There are notes throughout on 4.3BSD, the forthcoming system from the University of California at Berkeley. We trace the historical development of the UNIX system from its conception in 1969 until today, and describe the design principles that have guided this development. We then present the internal data structures and ...

2 Query evaluation techniques for large databases



Goetz Graefe

June 1993 ACM Computing Surveys (CSUR), Volume 25 Issue 2

Publisher: ACM Press

Full text available: pdf(9.37 MB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms, review

Database management systems will continue to manage large data volumes. Thus, efficient algorithms for accessing and manipulating large sets and sequences will be required to provide acceptable performance. The advent of object-oriented and extensible database systems will not solve this problem. On the contrary, modern data models exacerbate the problem: In order to manipulate large sets of complex objects as efficiently as today's database systems manipulate simple records, query-processi ...

Keywords: complex query evaluation plans, dynamic query evaluation plans, extensible database systems, iterators, object-oriented database systems, operator model of parallelization, parallel algorithms, relational database systems, set-matching algorithms, sort-hash duality

A high performance transparent bridge

Martina Zitterbart, Ahmed N. Tantawy, Dimitrios N. Serpanos August 1994 IEEE/ACM Transactions on Networking (TON), Volume 2 Issue 4

Publisher: IEEE Press



Full text available: pdf(1.41 MB) Additional Information: full citation, references, index terms

Birrell's distributed reference listing revisited

Luc Moreau, Peter Dickman, Richard Jones

November 2005 ACM Transactions on Programming Languages and Systems (TOPLAS), Volume 27 Issue 6

Publisher: ACM Press

Full text available: pdf(1.03 MB) Additional Information: full citation, abstract, references, index terms

The Java RMI collector is arguably the most widely used distributed garbage collector. Its distributed reference listing algorithm was introduced by Birrell et al. in the context of Network Objects, where the description was informal and heavily biased toward implementation. In this article, we formalize this algorithm in an implementationindependent manner, which allows us to clarify weaknesses of the initial presentation. In particular, we discover cases critical to the correctness of the alg ...

**Keywords**: Distributed garbage collection, distributed reference counting/listing, proof of correctness

5 High-speed switch scheduling for local-area networks

Thomas E. Anderson, Susan S. Owicki, James B. Saxe, Charles P. Thacker November 1993 ACM Transactions on Computer Systems (TOCS), Volume 11 Issue 4

**Publisher: ACM Press** 

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(2.37 MB) terms

Current technology trends make it possible to build communication networks that can support high-performance distributed computing. This paper describes issues in the design of a prototype switch for an arbitrary topology point-to-point network with link speeds of up to 1 Gbit/s. The switch deals in fixed-length ATM-style cells, which it can process at a rate of 37 million cells per second. It provides high bandwidth and low latency for datagram traffic. In addition, it supports real-time t ...

**Keywords:** ATM networks, iterative matching, statistical matching, switching scheduling

6 Wormhole IP over (connectionless) ATM

Manolis G. H. Katevenis, Iakovos Mavroidis, Georgios Sapountzis, Eva Kalyvianaki, Ioannis Mavroidis, Georgios Glykopoulos

October 2001 IEEE/ACM Transactions on Networking (TON), Volume 9 Issue 5

Publisher: IEEE Press

Full text available: pdf(211.25 KB)

Additional Information: full citation, abstract, references, citings, index terms

High-speed switches and routers internally operate using fixed-size cells or segments; variable-size packets are segmented and later reassembled. Connectionless ATM was proposed to quickly carry IP packets segmented into cells (AAL5) using a number of hardware-managed ATM VCs. We show that this is analogous to wormhole routing. We modify this architecture to make it applicable to existing ATM equipment: we propose a low-cost, single-input, single-output Wormhole IP Router that functions as a VP/ ...

Keywords: Connectionless ATM, IP over ATM, gigabit router, routing filter, wormhole IP, wormhole routing



A routing architecture for mobile integrated services networks

Shree Murthy, J. J. Garcia-Luna-Aceves

December 1998 Mobile Networks and Applications, Volume 3 Issue 4

Publisher: Kluwer Academic Publishers

Full text available: pdf(272.02 KB) Additional Information: full citation, abstract, references, index terms

A drawback of the conventional Internet routing architecture is that its route computation and packet forwarding mechanisms are poorly integrated with congestion control mechanisms. Any datagram offered to the network is accepted; routers forward packets on a best-effort basis and react to congestion only after the network resources have already been wasted. A number of proposals improve on this to support multimedia applications; a promising example is the Integrated Services Packet Networ ...

8 A unified wireless LAN architecture for real-time and non-real-time communication services

Sunghyun Choi, Kang G. Shin

February 2000 IEEE/ACM Transactions on Networking (TON), Volume 8 Issue 1

Publisher: IEEE Press

Additional Information: full citation, references, citings, index terms, <u>review</u>

Keywords: MAC protocol, QoS-sensitive communication, admission tests, dynamic timedivision duplexing (D-TDD), location-dependent errors, polling, priority scheduling, wireless LAN

9 Providing absolute differentiated services for real-time applications in static-priority scheduling networks



Shengquan Wang, Dong Xuan, Riccardo Bettati, Wei Zhao

April 2004 IEEE/ACM Transactions on Networking (TON), Volume 12 Issue 2

Publisher: IEEE Press

Full text available: pdf(519.25 KB) Additional Information: full citation, abstract, references, index terms

In this paper, we propose and analyze a methodology for providing absolute differentiated services for real-time applications. We develop a method that can be used to derive delay bounds without specific information on flow population. With this new method, we are able to successfully employ a utilization-based admission control approach for flow admission. This approach does not require explicit delay computation at admission time and, hence, is scalable to large systems. We assume the underlyi ...

**Keywords**: admission control, delay bound, differentiated services, priority assignment, real time, static-priority scheduling, utilization-based

10 Special issue: dasCMP'05: Hardware-modulated parallelism in chip multiprocessors



Julia Chen, Philo Juang, Kevin Ko, Gilberto Contreras, David Penry, Ram Rangan, Adam Stoler, Li-Shiuan Peh, Margaret Martonosi

November 2005 ACM SIGARCH Computer Architecture News, Volume 33 Issue 4

Publisher: ACM Press

Full text available: pdf(509.03 KB) Additional Information: full citation, abstract, references

Chip multi-processors (CMPs) already have widespread commercial availability, and technology roadmaps project enough on-chip transistors to replicate tens or hundreds of current processor cores. How will we express parallelism, partition applications, and schedule/place/migrate threads on these highly-parallel CMPs?This paper presents and evaluates a new approach to highly-parallel CMPs, advocating a new hardware-software contract. The software layer is encouraged to expose large amounts of mult ...

11 Implementation and evaluation of a QoS-capable cluster-based IP router Prashant Pradhan, Tzi-cker Chiueh

November 2002 Proceedings of the 2002 ACM/IEEE conference on Supercomputing

**Publisher: IEEE Computer Society Press** 

Full text available: pdf(215.68 KB) Additional Information: full citation, abstract, references, index terms

A major challenge in Internet edge router design is to support both high packet forwarding performance and versatile and efficient packet processing capabilities. The thesis of this research project is that a cluster of PCs connected by a high speed system area network provides an effective hardware platform for building routers to be used at the edges of the Internet. This paper describes a scalable and extensible edge router architecture called Panama, which supports a novel aggregate r ...

#### 12 Remote attribute grammars

John Tang Boyland

July 2005 Journal of the ACM (JACM), Volume 52 Issue 4

Publisher: ACM Press

Full text available: Topdf(764.29 KB) Additional Information: full citation, abstract, references, index terms

Describing the static semantics of programming languages with attribute grammars is eased when the formalism allows direct dependencies to be induced between rules for nodes arbitrarily far away in the tree. Such direct non-local dependencies cannot be analyzed using classical methods, which enable efficient evaluation. This article defines an attribute grammar extension ("remote attribute grammars") to permit references to objects with fields to be passed through the attribute ...

Keywords: Language description, collection attributes, remote attribution

#### 13 Transport protocol processing at GBPS rates

N. Jain, M. Schawrtz, T. Bashkow

August 1990 ACM SIGCOMM Computer Communication Review , Proceedings of the **ACM symposium on Communications architectures & protocols** SIGCOMM '90, Volume 20 Issue 4

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(1.55 MB) terms

This paper proposes an architecture for accomplishing transport protocol processing at Gbps rates. The limitations of currently used transport protocols have been analyzed extensively in recent literature. Several benchmark studies have established the achievable throughput of ISO TP4 and TCP to be in the low Mbps range; several new protocols and implementation techniques have been proposed to achieve 100 Mbps and higher throughput rates. We briefly review some of these protocols and establ ...

14 High speed switch scheduling for local area networks

Thomas E. Anderson, Susan S. Owicki, James B. Saxe, Charles P. Thacker September 1992 ACM SIGPLAN Notices , Proceedings of the fifth international conference on Architectural support for programming languages and operating systems ASPLOS-V, Volume 27 Issue 9

Publisher: ACM Press

Additional Information: full citation, references, citings, index terms

15 Cyclone: a broadcast-free dynamic instruction scheduler with selective replay

Dan Ernst, Andrew Hamel, Todd Austin

May 2003 ACM SIGARCH Computer Architecture News, Proceedings of the 30th annual international symposium on Computer architecture ISCA '03, Volume 31 Issue 2

**Publisher: ACM Press** 

Full text available: pdf(194.04 KB) Additional Information: full citation, abstract, references, citings

To achieve high instruction throughput, instruction schedulers must be capable of producing high-quality schedules that maximize functional unit utilization while at the same time enabling fast instruction issue logic. Many solutions exist to the scheduling problem, ranging from compile-time to run-time approaches. Compile-time solutions feature fast and simple hardware, but at the expense of conservative schedules. Dynamic schedulers produce high-quality schedules that incorporate run-time info ...

16 Virtual machine monitors: Xen and the art of virtualization

Paul Barham, Boris Dragovic, Keir Fraser, Steven Hand, Tim Harris, Alex Ho, Rolf Neugebauer, Ian Pratt, Andrew Warfield

October 2003 Proceedings of the nineteenth ACM symposium on Operating systems principles

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: <del>國 pdf(168.76 KB)</del>

Numerous systems have been designed which use virtualization to subdivide the ample resources of a modern computer. Some require specialized hardware, or cannot support commodity operating systems. Some target 100% binary compatibility at the expense of performance. Others sacrifice security or functionality for speed. Few offer resource isolation or performance quarantees; most provide only best-effort provisioning, risking denial of service. This paper presents Xen, an x86 virtual machine monit ...

**Keywords**: hypervisors, paravirtualization, virtual machine monitors

Guaranteed scheduling for switches with configuration overhead

Brian Towles, William J. Dally

October 2003 IEEE/ACM Transactions on Networking (TON), Volume 11 Issue 5

Publisher: IEEE Press

Full text available: pdf(709.45 KB) Additional Information: full citation, abstract, references, index terms

In this paper, we present three algorithms that provide performance guarantees for scheduling switches, such as optical switches, with configuration overhead. Each algorithm emulates an unconstrained (zero overhead) switch by accumulating a batch of configuration requests and generating a corresponding schedule for a constrained switch. Speedup is required both to cover the configuration overhead of the switch and to compensate for empty slots left by the scheduling algorithm. Scheduling algorit ...

**Keywords**: optical switches, packet switching

18 Special issue on persistent object systems: Adaptable pointer swizzling strategies in object bases: design, realization, and quantitative analysis Alfons Kemper, Donald Kossmann



July 1995 The VLDB Journal — The International Journal on Very Large Data Bases,

Volume 4 Issue 3

Publisher: Springer-Verlag New York, Inc.

Full text available: pdf(2.69 MB) Additional Information: full citation, abstract, references, citings

In this article, different techniques for "pointer swizzling" are classified and evaluated for optimizing the access to main-memory resident persistent objects. To speed up the access along inter-object references, the persistent pointers in the form of unique object identifiers (OIDs) are transformed (swizzled) into main-memory pointers (addresses). Pointer swizzling techniques can be divided into two classes: (1) those that allow replacement of swizzled objects from the buffer before th ...

**Keywords**: object-oriented database systems, performance evaluation, pointer swizzling

19 Virtual Memory

Peter J. Denning

September 1970 ACM Computing Surveys (CSUR), Volume 2 Issue 3

Publisher: ACM Press

Full text available: pdf(2.63 MB) Additional Information: full citation, references, citings, index terms

20 Cache Memories

Alan Jay Smith

September 1982 ACM Computing Surveys (CSUR), Volume 14 Issue 3

Publisher: ACM Press

Full text available: pdf(4.61 MB) Additional Information: full citation, references, citings, index terms

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